

AGILE TEAMING INFORMATION

<u>Organization</u>	<u>Areas of Expertise Keywords</u>
Xtreme EDA USA www.xtreme-eda.com Jim Nash jnash@xtreme-eda.com (508)-735-9929	1. Integrated Circuit Design 2. System on Chip Designs 3. Computer Architecture 4. Computer Modeling and Simulation 5. Interconnection Networks Other: ASIC, SoC, FPGA, RTL Design, Verification, SystemVerilog, UVM
Alion Science & Technology Corporation www.alionscience.com Brian Knight brian.knight@alionscience.com (540)-283-7557	1. Integrated Circuit Design 2. System on Chip Designs Other: 3PIP Analysis, FPGA Design & Verification, Trust & Assurance
George Washington University http://howie.seas.gwu.edu Howie Huang howie@gwu.edu N/A	1. Large Scale Graph Analytics 2. Accelerators 3. Machine Learning Algorithms 4. Memory Systems 5. Runtime Systems
RAM Laboratories, Inc. www.ramlabs.com Robert McGraw rmcgraw@ramlabs.com (619)-398-1410 x102	1. Machine Learning Algorithms 2. Computer Architecture 3. Non Von Neumann Architectures 4. Runtime Systems Other Keywords: Neuromorphic Computing
Perspecta Labs www.perspectalabs.com Josephine Micallef jmicallef@perspectalabs.com (908)-748-2599	1. Interconnection Networks 2. Machine Learning Algorithms 3. Runtime Systems 4. Large Scale Graph Analytics 5. Accelerators
GE Research www.ge.com/research Yamada Masako yamada@ge.com (518)-225-9261	1. Computer Modeling and Simulation 2. Large Scale Graph Analytics 3. Machine Learning Algorithms 4. Computer Architecture 5. System on Chip Designs
University of California - Davis https://sierra.ece.ucdavis.edu S.J. Ben Yoo sbyoo@ucdavis.edu	1. Interconnection Networks 2. Computer Architecture 3. Computer Modeling and Simulation

(510)-407-2457

University of Chicago
<http://lssg.cs.uchicago.edu>
Andrew Chien
achien@cs.uchicago.edu
(773)-834-0117

1. Computer Architecture
2. Machine Learning Algorithms
3. Memory Systems
4. Runtime Systems
5. Accelerators

Zero ASIC Corporation
www.zeroasic.com
Andreas Olofsson
andreas@zeroasic.com
(781)-325-6688

1. Computer Architecture
2. Memory Systems
3. Integrated Circuit Design
4. Accelerators
5. System on Chip Designs

New Jersey Institute of Technology
<http://www.njit.edu/>
David Bader
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1. Large Scale Graph Analytics
2. Accelerators
3. Non Von Neumann Architectures

Northrop Grumman Mission Systems
No website listed.
Joshua Watters
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1. Computer Architecture
2. Computer Modeling and Simulation
3. Accelerators
4. Non Von Neumann Architectures
5. Integrated Circuit Design

SRI International
www.sri.com
Michael Piacentino
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(609)-213-9899

1. Machine Learning Algorithms
 2. Computer Modeling and Simulation
 3. Non Von Neumann Architectures
- Other Keywords: Neuromorphic processing, Processor in Memory (PIM), hyperdimensional representations, semantic operations, rule-based logic representations.

Intelligent Automation, Inc (IAI)
<https://www.i-a-i.com/>
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(240)-406-7749

1. Computer Architecture
2. Memory Systems
3. Computer Modeling and Simulation
4. Runtime Systems
5. Machine Learning Algorithms

AIE
AENGINEERING.io
Kevin McMorro
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1. Computer Architecture
2. Computer Modeling and Simulation
3. Non Von Neumann Architectures
4. Runtime Systems
5. Memory Systems

Synopsys, Inc

1. Computer Modeling and Simulation

www.synopsys.com
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2. Integrated Circuit Design
 3. System on Chip Designs
 4. Interconnection Networks
 5. Accelerators
- Other Keywords: DTCO/STCO

Hewlett Packard Enterprise
<https://www.hpe.com>
Kirk Bresniker
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1. Interconnection Networks
2. Computer Modeling and Simulation
3. Computer Architecture
4. Accelerators
5. Non Von Neumann Architectures

APIC Corporation
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1. Computer Architecture
 2. Computer Modeling and Simulation
 3. Integrated Circuit Design
 4. Interconnection Networks
 5. Large Scale Graph Analytics
- Other Keywords: Silicon-Photonics; Heterogenous Integration of Photonics

Tactical Computing Laboratories
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1. Computer Architecture
 2. Computer Modeling & Simulation
 3. System on Chip Designs
 4. Accelerators
 5. Memory Systems
- Other Keywords: LLVM Compiler Infrastructure, SST simulator

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1. Computer Architecture
2. Memory Systems
3. Non Von Neumann Architectures
4. Accelerators
5. Interconnection Networks

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1. Computer Modeling and Simulation
 2. Non Von Neumann Architectures
 3. Large Scale Graph Analytics
 4. Runtime Systems
 5. Interconnection Networks
- Other Keywords: HW/SW codesign, compiler optimization, performance optimization

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gemstate.ai
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1. Computer Architecture
2. Large Scale Graph Analytics
3. Runtime Systems
4. Memory Systems
5. Accelerators

Other Keywords: Graph500, Modeling & Simulation

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1. Computer Architecture
 2. Memory Systems
 3. Runtime Systems
 4. Non Von Neumann Architectures
 5. Large Scale Graph Analytics
- Other Keywords: Parallel Computing

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1. Computer Architecture
2. Integrated Circuit Design
3. Machine Learning Algorithms
4. Computer Modeling and Simulation
5. Large Scale Graph Analytics

Areas Sought Keywords

N/A. Only other keywords: Physical Design, Place & Route, GDSII

1. Computer Architecture
 2. Computer Modeling and Simulation
 3. Interconnection Networks
 4. Large Scale Graph Analytics
 5. Machine Learning Algorithms
- Other Keywords: All other capability gaps

1. System on Chip Design
 2. Integrated Circuit Design
 3. Non Von Neumann Architectures
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1. Memory Systems
2. Large Scale Graph Analytics
3. Integrated Circuit Design
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1. Large Scale Graph Analytics
2. Integrated Circuit Design

1. Large Scale Graph Analytics
2. Machine Learning Algorithms
3. Runtime Systems
4. Computer Modeling and Simulation

N/A

1. Runtime Systems
2. Non Von Neumann Architectures
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4. Memory Systems
5. Computer Architecture

N/A

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1. Computer Architecture

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1. Non Von Neumann Architectures
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1. Machine Learning Algorithms
2. Large Scale Graph Analytics
3. Computer Modeling and Simulation
4. Integrated Circuit Design
5. Runtime Systems

1. System on Chip Designs
 2. Memory Systems
- Other Keywords: RTL-compatible simulation models, open source HLS toolchains

1. Interconnection Networks
2. Integrated Circuit Design

1. Interconnection Networks
2. Computer Modeling and Simulation
3. Integrated Circuit Design
4. Machine Learning Algorithms

1. Interconnection Networks
2. Large Scale Graph Analytics
3. Other Keywords: Packaging